

display apparatus, and the drive IC itself is identical in configuration to that shown in Figure 15.

That is, the drive IC 3 employs a CMOS configuration using pMOS transistors for pullup-side driving devices 60-1 to 60-d and nMOS transistors for pulldown-side driving devices 70-1 to 70-d, and the pullup- and pulldown-side driving devices are driven from the driving stages 600 and 700, respectively.

# IN THE CLAIMS:

Please REPLACE claims, in accordance with the following:

1. (AS ONCE AMENDED) A capacitive-load driving circuit, comprising:  
a driving device connecting a driving power supply source to an output terminal connectable to a capacitive load; and  
a power distributing circuit connected between the driving power supply source and the driving device.

9. (AS ONCE AMENDED) A capacitive-load driving circuit, comprising:  
a driving device connecting a reference potential point to an output terminal ; and  
a power distributing circuit inserted between the reference potential point and the driving device.

17. (AS ONCE AMENDED) A capacitive-load driving circuit, comprising:  
a plurality of driving devices driving a plurality of capacitive loads and formed in integrated-circuit form ; and  
a power distributing circuit connecting each of the plurality of driving devices to a driving power supply source or to a reference potential point .

41. (AS ONCE AMENDED) A plasma display apparatus having a capacitive-load driving circuit, comprising:  
a driving device connecting a driving power supply source to an output terminal ; and  
a power distributing circuit connected between the driving power supply source and the driving device.

45. (AS ONCE AMENDED) A plasma display apparatus having a capacitive-load